

~~CLAIMS:~~

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1. A recording device (1) for recording digital recording data (AD) in a recording mode of the recording device (1) with data bus means (10) to which a digital data bus (9) can be connected and which are designed for receiving input data (ED1-ED9) transmitted in at least a first input channel (C1(STB), C5(TV), C20(DV), C25(DV), C33(PC), C34(PC)) of the data bus (9), and with

processing means (16) for processing received input data (ED1-ED9) and for providing recording data (AD), and with

recording means (17) for recording the recording data (AD) on a data carrier (18) in the recording mode, characterized in that

a record-prepared mode can be activated in the recording device (1), and in that

test means (20) are provided for testing whether input data (ED1-ED9) are being received in the first input channel (C1(STB), C5(TV), C20(DV), C25(DV), C33(PC), C34(PC)), and in that,

when the record-prepared mode is activated, the test means (20) are capable of activating the recording mode for recording as recording data (AD) input data (ED1-ED9) received in the first and/or in at least one second input channel (C1(STB), C5(TV), C20(DV), C25(DV), C33(PC), C34(PC)) for the time duration of reception of input data (ED1-ED9) in the first input channel (C1(STB), C5(TV), C20(DV), C25(DV), C33(PC), C34(PC)).

2. A recording device (1) as claimed in claim 1, characterized in that the data bus means (10) are designed for receiving input data (ED1-ED9) of a digital data bus (9) complying with the 1394 standard (IEEE Standard for a High Performance Serial Bus, IEEE Std 1394-1995, SH94364).

3. A recording device (1) as claimed in claim 2, characterized in that the test means (20), while testing whether input data (ED1-ED9) are being received in a first input channel (C1(STB), C5(TV), C20(DV), C25(DV), C33(PC), C34(PC)), are designed for testing a "Channels_Available Register" as laid down in the 1394 standard.

4. A recording device (1) as claimed in claim 2, characterized in that the data bus means (10) are designed for receiving input data (ED1-ED9) of a digital data bus (9) which complies with the IEC61883 standard (Digital interface for consumer audio/video equipment: Part 1, Part 4; IEC61883).

5. A recording device (1) as claimed in claim 4, characterized in that the test means, while testing whether input data (ED1-ED9) are being received in a first input channel (C1(STB), C5(TV), C20(DV), C25(DV), C33(PC), C34(PC)), are designed for testing a "CIP Header" as laid down in the IEC61883 standard.

6. A recording device (1) as claimed in claim 1, characterized in that a selection memory (21) is provided in which a channel number information (CI) which characterizes at least the first input channel (C1(STB), C5(TV), C20(DV), C25(DV), C33(PC), C34(PC)) and/or a channel number information (CI) which characterizes at least the second input channel (C1(STB), C5(TV), C20(DV), C25(DV), C33(PC), C34(PC)) can be stored.

7. A recording device (1) as claimed in claim 6, characterized in that a priority information (PI) associated with at least one channel number information (CI) can be stored in the selection memory (21), which priority information indicates the priority of the recording of input data (ED1-ED9) received in the input channel (C1(STB), C5(TV), C20(DV), C25(DV), C33(PC), C34(PC)) characterized by the channel number information (CI).

8. A recording device (1) as claimed in claim 1, characterized in that data rate comparison means (23) are provided which are designed for comparing the actual data rate of the recording data (AD) recorded by the recording means (17) with a maximum data rate being the maximum quantity of recording data (AD) which can be recorded by the recording means (17), and in that the recording means (17) are capable of recording input data (ED1-ED9) from several input channels (C1(STB), C5(TV), C20(DV), C25(DV), C33(PC), C34(PC)) up to said maximum quantity of recording data (AD) corresponding to the maximum data rate.

9. A recording device (1) as claimed in claim 1, characterized in that transcoding means (24) are provided for transcoding received input data (ED1-ED9) coded in accordance with a first coding system (MPEG, DVCR) into processed input data (ED1-ED9) coded in accordance with a second coding system (MPEG, DVCR) with a reduced data rate.

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10. A recording device (1) as claimed in claim 1, characterized in that the processing means (16) are suitable for processing and outputting and the recording means (17) are suitable for recording of recording data (AD) which comply with the DVHS standard (D-VHS System Standard, October 1997).

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11. A method of recording digital recording data (AD) by means of a recording device (1), which method comprises the following steps:

receiving of input data (ED1-ED9) transmitted in at least a first input channel (C1(STB), C5(TV), C20(DV), C25(DV), C33(PC), C34(PC)) of a digital data bus (9),

15 processing of the received input data (ED1-ED9) and supplying recording data (AD), and

recording the recording data (AD) on a data carrier (18), characterized in that it is tested in the case of an activated record-prepared mode in the recording device (1) whether input data (ED1-ED9) are being received in the first input channel (C1(STB), C5(TV), C20(DV), C25(DV), C33(PC), C34(PC)), and in that the input data (ED1-ED9) received in the first and/or in at least one second input channel (C1(STB), C5(TV), C20(DV), C25(DV), C33(PC), C34(PC)) are recorded as recording data (AD) for the duration of reception of input data (ED1-ED9) in the first input channel (C1(STB), C5(TV), C20(DV), C25(DV), C33(PC), C34(PC)).

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